



Any Docket No.: 004363.P004

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In RE Application of:

Mar Hershenson, et al.

Serial No.: 09/843,486

Filing Date: April 25, 2001

For: Optimal Simultaneous Design And  
Floorplanning Of Integrated Circuit

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, V.A. 22313-1450

Examiner: Steven Loke

Art Unit: 2811

Confirmation No.: 9275

I hereby certify that this correspondence is being deposited  
with the United States Postal Service as first class mail with  
sufficient postage in an envelope addressed to the  
Commissioner for Patents, P.O. Box 1450, Alexandria, VA,  
22313-1450, on

(Date of Deposit)

Carla Vignola

(Printed name)

(Signature)

(Date)

AMENDMENT UNDER 37 C.F.R. § 1.312 AFTER ALLOWANCE

**IMPORTANT REQUEST PRESENTED IN THE COMMENTS AFTER AMENDMENTS**

Dear Sir:

Although the above-identified application has been allowed, the Applicants respectfully request that the application be amended pursuant to 37 C.F.R. § 1.312 as indicated in the following paragraphs.